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Class	Subclass	ISSUE CLASSIFICATION



PATENT NUMBER

U.S. UTILITY Patent Application

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/723563		257	779	2826 2811	Williams

APPLICANTS:  
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TITLE: Input/output architecture for integrated circuits

PTO-2040  
12/99

(Date)	(Assigned Examiner)	(Date)
(Date)	(Primary Examiner)	(Date)
(Date)	(Legal Instruments Examiner)	(Date)

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